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Abstract

The time precision achievable using standard analog methods is well known. Several expressions for timing methods using digitized signals have been recently proposed in workshops, conferences or training courses. Most of them are only partially exact. This paper presents a comprehensive calculation of the timing precision for algorithms using digital treatment of digitized signal to mimic analog discriminator-based methods. The results of these calculations are discussed for various cases of correlation between samples.

Keywords: Time picking methods, Waveform sampling, CFD, dCFD, SCA, TDC

1. Introduction

Time picking of pulses with high precision of the order of few ps is a key challenge for the next generation high energy physics experiments. Since decades, precise timing is usually obtained using the combination of an analog discriminator followed by a device measuring the time of arrival of its output and digitizing it (Time to Digital converter or Time to Amplitude converter followed by an ADC). The discriminator can be a simple leading edge one or a more sophisticated one. One major effect affecting the precise timing measurement is the time walk, ie the dependency of the timing with the input pulse amplitude, which can be partially canceled if a Constant Fraction Discriminator structure [1] is used or corrected after calibration using

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the pulse's amplitude or Time Over Threshold information [2]. Continuous progress of commercial Analog to Digital converter up to few 100 MSPS and the availability of second [3], [4] and third generation analog memories [5] now allow, for a moderate cost, to sample and digitize directly the analog signal at high frequency. The digitized samples can then be digitally processed to extract the pulse's timing. Among the possible algorithm, the digital CFD dCFD, which mimics the analog CFD behavior is very attractive because it requires only moderate computing resources for performance often similar to those obtained with more complex algorithms [6]. If the theoretical expression of the time precision obtained using an analog method is well known, the one used for dCFD algorithm is often under or over-estimated. An exact calculation of it is presented in this paper, taking into account the correlation effect between samples.

2. Time resolution of the leading edge discriminator

2.1. Analog leading edge discriminator, relationship between voltage noise and jitter

The simpler time picking method, the Leading Edge Discrimination (LED), illustrated by Figure 1, consists in measuring the (t_p) time of the point P at which the analog input signal crosses a fix threshold (Th).

As shown on Figure 2, if the signal is noisy with a noise standard deviation $\sigma_{\rm A}$ and if we consider that the signal is linear around P with a dA/dt slope, we can express $\sigma_{\rm t}$, the standard deviation of the threshold crossing time (jitter) thanks Equation 1:

$$\sigma_t = \frac{\sigma_A}{dA/dt} \tag{1}$$

Conversely, if the time of P is measured with a limited precision (σ_j) - by a TDC for instance - Equation 1, allows us to translate it in an equivalent voltage noise. In both cases, we have to remember that this equation can only be used if the local linear approximation of the signal remains valid for the amplitude of the noise.

As the TDC jitter and the voltage noise are normally uncorrelated, we can express the total standard variance on the time by Equation 2

$$\sigma_{tt}^2 = \sigma_j^2 + \left(\frac{\sigma_A}{dA/dt}\right)^2 \tag{2}$$

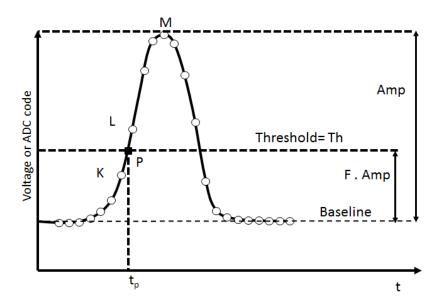


Figure 1: Principle of LED, dLED, CFD and dCFD

This two equations are only valid for an input signal with a fix amplitude. If the amplitude varies, the timing is affected by the time walk effect that is not taken into account here.

2.2. Digital leading edge discriminator, effect of interpolation

If the analog input signal is digitized at a F rate (corresponding to a T sampling period), it is possible, as shown on Figure 1, to digitally mimic the LED method by interpolating between the two samples K and L around the threshold crossing to find the crossing point P. With this Digital Leading Edge Discrimination method (dLED), as we use at least two samples to find the timing of the crossing, it is natural to think that the timing resolution of a noisy signal can be improved compared to the LED case. In this subsection, we will evaluate this time resolution improvement assuming that the signal is linear between the K an L samples.

As shown on Figure 3, for simplicity, the coordinates of K are (0,0). Those of L are (T, TdA/dt) where dA/dt represents the slope of the signal and T the sampling period. If we consider now consider that the input signal is noisy, K and L, respectively affected by $\delta y1$ and $\delta y2$ noise voltages, become K' and L'. Using K' and L' to interpolate the values at the time t leads to an

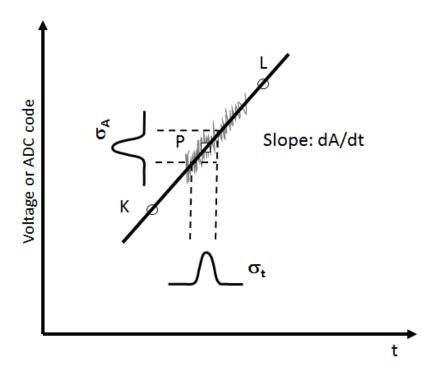


Figure 2: Voltage noise and jitter equivalence

error ΔY compared to the voltage for the same time on the original segment (K,L).

$$\Delta Y = \partial y 2 \frac{t}{T} + \partial y 1 \frac{T - t}{T} \tag{3}$$

The variance of this error is given by:

$$Var(\Delta Y) = \frac{(T-t)^2 \ var(\partial y1) + t^2 \ var(\partial y2) + 2 \ t \ (T-t) \ cov \ (\partial y1, \partial y2)}{T^2}$$

$$\tag{4}$$

Integrating Equation 4 over a sampling period allows to calculate the average error introduced by the noise.

$$\overline{Var(Y)} = \frac{1}{T} \int_0^T Var(Y) dt \tag{5}$$

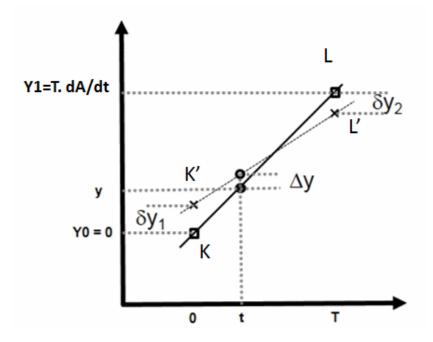


Figure 3: Interpolation between two noisy samples

$$\overline{Var(Y)} = \frac{1}{T^3} \left\{ var(\partial y1) \left[T^2t + \frac{t^3}{3} - Tt^2 \right]_0^T + var(\partial y2) \left[\frac{t^3}{3} \right]_0^T + cov(\partial y1, \partial y2) \left[Tt^2 - 2\frac{t^3}{3} \right]_0^T \right\}$$

$$(6)$$

$$\overline{Var(Y)} = \frac{1}{3}var(\partial y1) + \frac{1}{3}var(\partial y2) + \frac{1}{3}cov(\partial y1, \partial y2)$$
 (7)

that can be reduced to Equation 8 if we make the hypothesis of noise ergodicity so that the noise variance σ_n^2 is the same for the two consecutive samples

$$\overline{Var(Y)} = \frac{2}{3}\sigma_n^2 + \frac{1}{3}\cos(\partial y1, \partial y2)$$
 (8)

If the noise standard deviation is negligible compared to ΔY ¹, this equation can be used to calculate the variance of the timing precision by applying

¹so that dA/dt is not affected.

Equation 1, leading to:

$$\overline{Var(t_{p,n})} = \frac{\frac{2}{3}\sigma_n^2 + \frac{1}{3}\cos(\partial y_1, \partial y_2)}{(dA/dt)^2}$$
(9)

As in the previous section, we can also consider that the K and L samples are affected by uncertainties due to jitter, respectively $\delta t1$ and $\delta t2$, with a common value of standard deviation σ_j . If we make the assumption that σ_j is negligible compared to T^2 , the contribution of these jitters to the variance of the interpolated crossing time can be calculated using successively 1. the reversed form of Equation 1,2. Equation 9 and finally 3. Equation 1, leading to

$$\overline{Var(t_{p,j})} = \frac{2}{3}\sigma_j^2 + \frac{1}{3}\cos(\partial t 1, \partial t 2)$$
(10)

In the general case, the noise voltage and sampling jitter are uncorrelated so that the total variance of the interpolated crossing time can be obtained by summing the right terms of Equations 9 and 10.

$$\overline{Var(t_p)} = \overline{Var(t_{p,n})} + \overline{Var(t_{p,j})}$$
(11)

2.3. Interpolation: various cases of correlation between samples

The results of Equations 9 and 10 are obviously dependent of the correlation between samples.

2.3.1. Case of uncorrelated samples

If the fluctuations in amplitude of the two samples are totally uncorrelated, which is the case for:

- an unfiltered white noise (high frequency noise compared to the sampling frequency),
- the part of noise associated to the sampling, quantization and digitizing processes,

Equation 9 can be reduced to:

$$\overline{Var(t_{p,n})} = \frac{2}{3} \frac{\sigma_n^2}{(dA/dt)^2}$$
(12)

²again so that dA/dt is not affected.

Similarly, if the fluctuation of time sampling are uncorrelated between samples, which can be the case for example for a properly generated clock of an ADC, Equation 10 can be reduced to:

$$\overline{Var(t_{p,j})} = \frac{2}{3}\sigma_j^2 \tag{13}$$

Both equations are showing that the use of the two uncorrelated noisy samples permits to improve (through interpolation) the time picking resolution by a factor $\sqrt(3/2) = 1.23$ compared to the analog method. Note that it is significantly less than the $\sqrt{2}$ factor often used.

2.3.2. Case of totally correlated samples

If the voltage noise is totally correlated between the two samples as it is the case for low-pass-filtered-white noise over-sampled:

Equation 9 becomes:

$$\overline{Var(t_{p,n})} = \frac{\sigma_n^2}{(dA/dt)^2} \tag{14}$$

Similarly if the fluctuation of time sampling are totally correlated between samples, which can be partially the case in the SCA in which a single master clock is successively delayed to obtain the consecutive sampling commands, Equation 10 becomes:

$$\overline{Var(t_{p,j})} = \sigma_j^2 \tag{15}$$

In this case there is no improvement compared to the analog method.

2.3.3. Case of anticorrelated samples

If the covariance term between samples is negative (anti-correlated fluctuation between consecutive samples) the improvement factor, compared to the analog method, can be as high as $\sqrt{3}$ in the case of fully anti-correlated samples³. But this case seems to be practically very unlikely.

 $^{^3{\}rm conversely}$ to all the other tested cases, this value is not confirmed by Monte-Carlo simulations that give an improvement factor of 2

2.3.4. Case of partially correlated samples

As an example, let us examine the case of a voltage white noise filtered by low pass filter with a τ time constant. The covariance term of Equation 9 is derived from the noise autocorrelation function calculated in [7] giving :

$$\overline{Var(t_{p,n})} = \frac{\frac{2}{3}\sigma_n^2 + \frac{1}{3}\sigma_n^2 exp(-T/\tau)}{(dA/dt)^2}$$
(16)

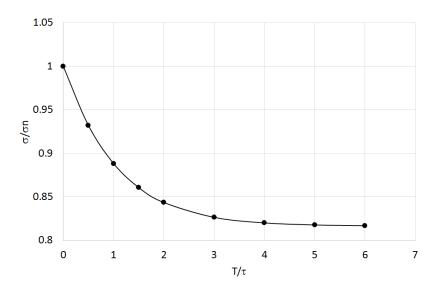


Figure 4: Dependency of the noise at interpolated crossing point vs T/τ in the case of a white noise filtered using a lowpass filter with time τ time constant

The dependency of the inverse of the improvement factor on the standard deviation of the interpolated amplitude at the crossing point (identical to the one of the interpolated crossing timing) with respect to the one obtained with the analog method is plotted on Figure 4 as function of the T/τ ratio. If we consider the case of a pulse (output of a charge sensitive amplifier) filtered by a single pole lowpass filter, its rise time is given by 2.2 τ . To extract its timing we need more than 2 samples on the signal rising edge. It means that the T/τ ratio is smaller that 1 so that the samples used for the interpolation are strongly correlated and that the noise improvement factor will be of only 10% or less.

2.4. Digital CFD

The dCFD algorithm, also illustrated by Figure 1 mimics the behavior of a constant fraction discriminator. For this purpose the amplitude Amp of the digitized pulse is determined using a digital algorithm (for example parabolic interpolation around the sample M). A threshold is set to a fraction $F \times Amp$ of this amplitude, and the signal timing is determined by the crossing point of the interpolated digitized signal and this threshold. As for analog CFD, this method is used to reduce the time walk effect.

The variance of the interpolated timing can be derived from Equation 9 by adding an extra contribution corresponding to the threshold fluctuation equal to the fraction F of those of the peak amplitude. Because of the filtering effect of the digital treatment allowing its extraction, the standard deviation of the amplitude is $\alpha \times \sigma_n$ with $\alpha \leq 1$. If we neglect the noise correlation between the threshold crossing and the peak ⁴, we can calculate the variance of the interpolated crossing time for dCFD as:

$$\overline{Var(t_p)} = \frac{\frac{2}{3}(1 + F^2\alpha^2)\sigma_n^2 + \frac{1}{3}\cos(\partial y_1, \partial y_2)}{(dA/dt)^2} + \frac{2}{3}\sigma_j^2 + \frac{1}{3}\cos(\partial t_1, \partial t_2)$$
(17)

3. Practical case of a SCA

Now let us study the case of a typical SCA-based digitizer as SAMPIC. The "voltage" noise digitized by the chain is the quadratic sum of :

- a voltage noise, with σ_n standard deviation and a correlation between consecutive samples depending on the noise spectrum quantified by their covariance (here noted as cov(T))
- the sampling, quantization and digitization noise, with σ_d standard deviation uncorrelated from sample to sample

The time sampling precision is the combination of:

⁴this correlation obviously exists but is much more smaller than between two consecutive samples. As it is likely positive, it will improve very slightly the variance given by Equation 17

- the jitter on the master clock, with $\sigma_{\rm ck}$ standard deviation, introducing a correlation on the timing of the N consecutive delays of the DLL providing the sampling signal. The covariance term quantifying the correlation between the two consecutive samples is $\sigma_{\rm ck}^2(N-1)/N \sim \sigma_{\rm ck}^2$
- the jitter σ_{step} introduced by the delay steps, which can be considered as uncorrelated from sample to sample

Equation 17 then becomes:

$$\overline{Var(t_p)} = \frac{\frac{2}{3}(1 + F^2\alpha^2)(\sigma_n^2 + \sigma_d^2) + \frac{1}{3}\cos(T)}{(dA/dt)^2} + \frac{2}{3}\sigma_{step}^2 + \sigma_{ck}^2$$
 (18)

4. Conclusion

Theoretical expression for the timing resolution achievable using dLED and dCFD with linear interpolation methods have been calculated assuming:

- A linear signal between samples,
- Voltage noises and time jitters small compared to respectively the voltage difference between the 2 interesting consecutive samples and the sampling period.

It has been shown that these methods permits to improve the timing by only up to 20% compared to the basic analog method. This improvement decreases when the consecutive samples are correlated.

Even if only the linear interpolation and a piecewise linear signal have been studied, the trends identified in this paper will remain valid in a more general case.

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[1] D.A. Gedcke and W.J. McDonald. A constant fraction of pulse height trigger for optimum time resolution. *Nuclear Instruments and Methods*, 55:377 – 380, 1967.

- [2] E. Delagnes, P. Abbon, Y. Bedfer, J. C. Faivre, F. Kunne, A. Magnon, S. Platchkov, P. Rebourgerad, and D. Thers. Sfe16, a low noise front-end integrated circuit dedicated to the read-out of large micromegas detectors. *IEEE Transactions on Nuclear Science*, 47(4):1447–1453, Aug 2000.
- [3] D. Breton, E. Delagnes, J. Maalmi, and P. Rusquart. The wavecatcher family of sca-based 12-bit 3.2-gs/s fast digitizers. In *Real Time Conference* (RT), 2014 19th IEEE-NPSS, pages 1–8, May 2014.
- [4] Stefan Ritt, Roberto Dinapoli, and Ueli Hartmann. Application of the {DRS} chip for fast waveform digitizing. Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 623(1):486 488, 2010. 1st International Conference on Technology and Instrumentation in Particle Physics.
- [5] E. Delagnes, D. Breton, H. Grabas, J. Maalmi, and P. Rusquart. Reaching a few picosecond timing precision with the 16-channel digitizer and timestamper SAMPIC ASIC. Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 787:245 – 249, 2015.
- [6] D. Breton, E. Delagnes, J. Maalmi, K. Nishimura, L.L. Ruckman, G. Varner, and J. Va'vra. High resolution photon timing with mcp-pmts: A comparison of a commercial constant fraction discriminator (cfd) with the asic-based waveform digitizers {TARGET} and wavecatcher. Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 629(1):123 132, 2011.
- [7] J. Loren Passmore, Brandon C. Collings, and Peter J. Collings. Autocorrelation of electrical noise: An undergraduate experiment. *American Journal of Physics*, 63(7):592–595, 1995.